

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 245112US-2DIV		SERIAL NO. <u>12/713044</u> New Divisional Application	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Takashi IPPOSHI et al.			
				FILING DATE Herewith		GROUP Not Yet Assigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
T1	AA	5,343,087	30 Aug 1994	T. FURUYAMA	—	—	
T1	AB	5,698,869	16 Dec 1997	M. YOSHIMI et al.	—	—	
T1	AC	5,838,609	17 Nov 198	H. KURIYAMA	—	—	
T1	AD	5,801,080	1 Sept 1998	Y. INOUE et al.	—	—	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	Hirano, Y. et al., "Bulk-Layout-Compatible 0.18.m SOI-CMOS Technology Using Body-Fixed Partial Trench Isolation (PTI)," <u>IEEE International SOI Conference</u> , Oct. 1999, pp. 131.132.					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <u>Tim KMC</u>					Date Considered <u>12/28/2004</u>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							